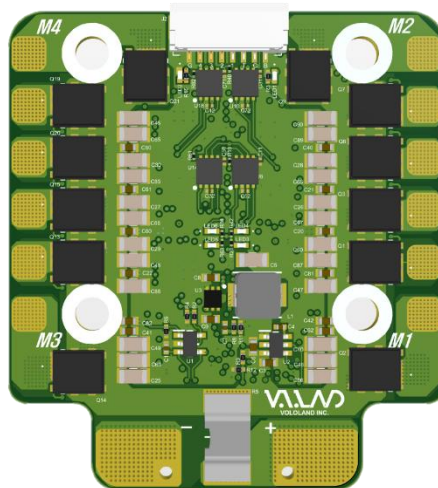


NarinESC_70A

Manual

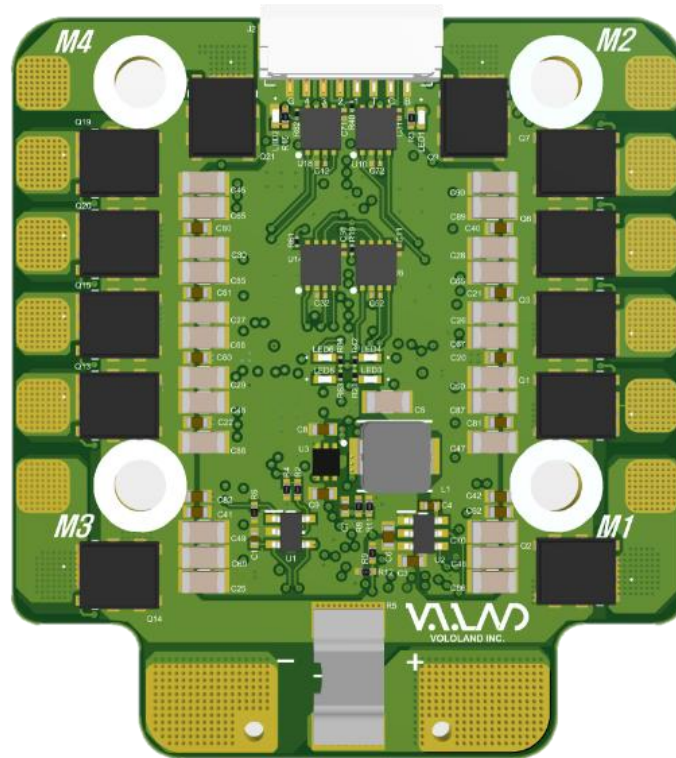


1. Basic Parameters

Main FMU Processor	• STM32L431
Gate drive	• MP6531
Firmware	• AM32 Firmware
Power Input	• 3S - 6S
Current Per Motor	• 70A Continue • Burst Current (TBD)
Interface	• 8P JST-GH • 3P JST-GH
Dimensions	• 50.00mm x 55.00mm x 7.00mm
Mounting Pattern	• 31.00mm x 30.00mm
Weight	• TBD

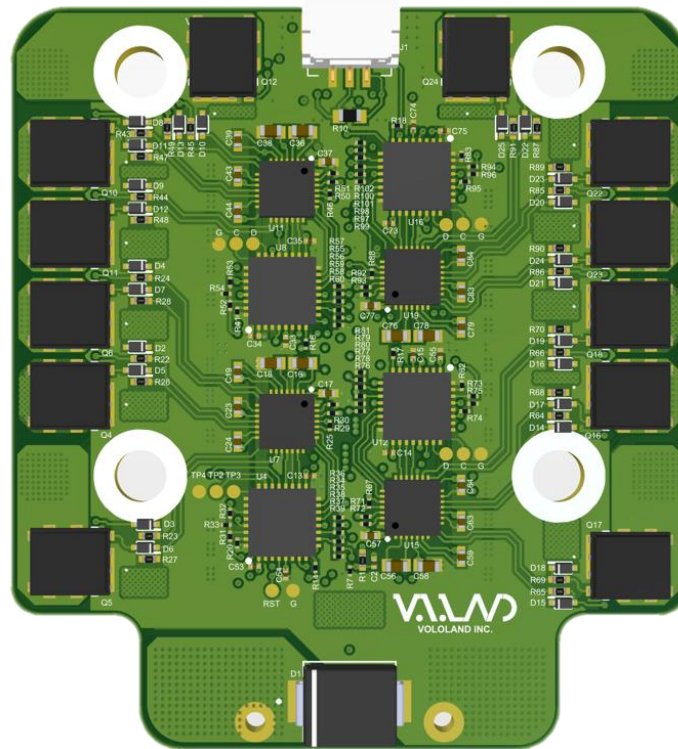
[Parameter]

2. Pinout



Flight Controller Connector – 8 Pin JST-GH

Pin	Signal	Voltage
1	VBAT	VBAT
2	CURR	3.3Vdc
3	TELEM	3.3Vdc
4	MOTOR1	3.3Vdc
5	MOTOR2	3.3Vdc
6	MOTOR3	3.3Vdc
7	MOTOR4	3.3Vdc
8	GND	GND



CAN Connector – 3 Pin JST-GH

Pin	Signal	
1	CAN_H	
2	CAN_L	
3	GND	

3. Firmware - TBD