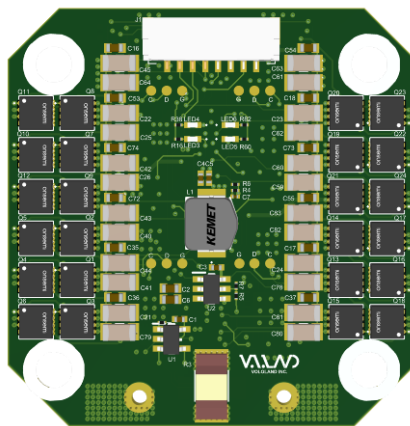


NarinESC_40A

Manual

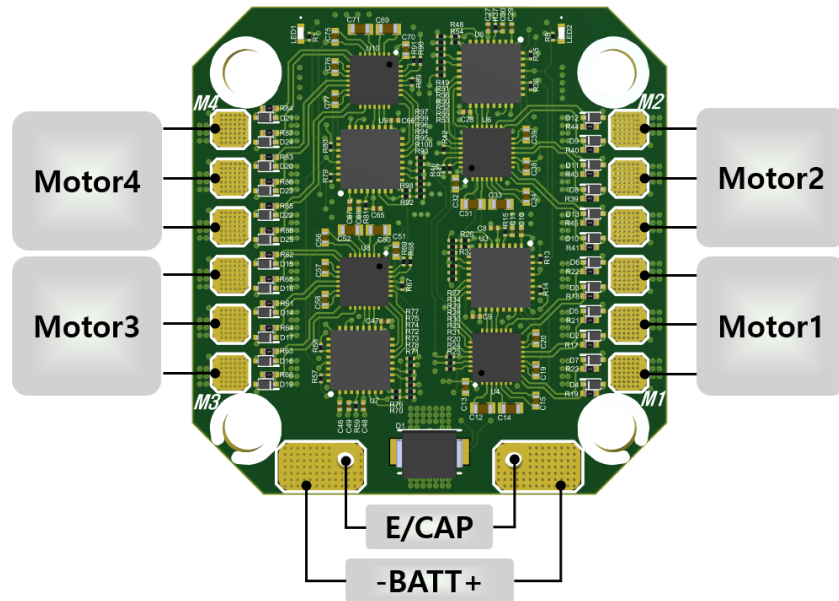


1. Basic Parameters

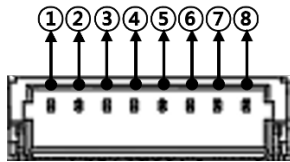
Main FMU Processor	• STM32F051
Gate drive	• MP6531
Firmware	• AM32 Firmware
Power Input	• 3S - 6S
Current Per Motor	• 40A Continue • Burst Current (TBD)
Interface	• 8P JST-GH
Dimensions	• 40mm x 41mm x 7mm
Mounting Pattern	• 31.00mm x 30.00mm
Weight	• 14g

[Parameter]

2. Wire Diagram



3. Pinout



①	BATT IN
②	ADC 1
③	UART8_TX
④	PWM 1
⑤	PWM 2
⑥	PWM 3
⑦	PWM 4
⑧	GND

4. Firmware - TBD